



KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE :: WARANGAL-15

(An Autonomous Institute under Kakatiya University, Warangal)

DEPARTMENT OF ELECTRONICS AND INSTRUMENTATION ENGINEERING

M.Tech. CLASSWORK TIMETABLE for the ODD SEMESTER of Academic Year 2021-22

VLSI&ES
PRR-20

III SEMESTER of M.Tech (VLSI&ES)

Class Teacher: Sri O. Anjaneyulu

w.e.f. 22.09.2021

Day	9.40am - 10.30am	10.30am -11.20am	11.20am -12.10pm	12.10pm - 1.00pm		2.00pm - 2.50pm	2.50pm - 3.40pm	3.40pm - 4.30pm
Mon	DP-I	DP-I	DP-I	DP-I	L U N C H	IS/ RES SH--3-4/SH-2-6	DFT BVI-308	DFT(T) BVI-308
Tue	DP-I	DP-I	DP-I	DP-I		IS/ RES SH--3-4/SH-2-6	DFT BVI-308	DP-I
Wed	DP-I	DP-I	DP-I	DP-I		DFT BVI-308	IS/ RES SH--3-4/SH-2-6	IS(T)/ RES (T) SH--3-4/SH-2-6
Thu	DP-I	DP-I	←-----IE-----→ BJ			DP-I	DP-I	DP-I
Fri	-	-	-	-		-	-	-
Sat	-	-	-	-		-	-	-

Course Title		Course Code & Course Name		Course Faculty
DFT(PE-V)	:	P20VE301A: Design for Testability	-	Sri O. Anjaneyulu
(OE-I): IS / RES	:	P20OE302B: Industrial Safety / P20OE302G: Renewable Energy Sources	-	Dr. M. B. Sushma / Dr. V. Ashok
DP-I		P20VE303 : Dissertation Phase-I	-	Sri B. Venumaheshwar(BVM)
IE	:	P20VE304: Internship Evaluation	-	Sri B. Jeevan(BJ)

Sd/-
Faculty I/c, Dept .Time Tables

Sd/-
Faculty I/c, Overall Time Tables

Sd/-
Head of the Department

Sd/-
Dean, AA

Sd/-
Principal